

1 1. A method comprising:
2 providing error data that indicate motion in an image;
3 representing error data as a collection of ordered bits; and
4 coding the bits of each order to indicate zerotree roots that are associated
5 with the order.

1 2. The method of claim 1, wherein the act of coding the bits comprises:
2 determining which of the bits indicate zeros; and
3 classifying each zero as either an isolated zero or a zerotree root.

1 3. The method of claim 2, wherein some of the error data are descendants of
2 some of the other error data, and wherein the act of determining comprises:
3 traversing a descendant tree from a bit associated with one of said some of
4 the error data to bits associated with said other error data to locate the zerotree roots.

1 4. The method of claim 1 wherein providing error data includes taking the
2 difference between two successive image representations in an image sequence.

1 5. The method of claim 4 wherein taking the difference includes taking the
2 difference of two successive discrete wavelet transform coded frames.

1 6. The method of claim 1 including coding said bits based on whether or not
2 the data exceeds a predetermined threshold value.

1 7. An article comprising a storage medium readable by a processor-based
2 system, the storage medium storing instructions to enable a processor to:

3 ~~provide error data that indicate motion in an image,~~
4 represent error data as a collection of ordered bits, and
5 code the bits of each order to indicate zerotree roots that are associated
6 with the order.

1 8. The article of claim 7, the storage medium comprising instructions to
2 enable the processor to:
3 determine which of the bits indicate zeros, and
4 classify each zero as either an isolated zero or a zerotree root.

1 9. The article of claim 8 wherein some of the error signals are descendants of
2 some of the other error signals, the storage medium comprising instructions to enable the
3 processor to:
4 traverse a descendant tree from a bit associated with one of said some of
5 the error data to bits associated with said other error data to locate the zerotree roots.

1 10. The article of claim 7 wherein the storage medium comprising instructions
2 to enable the processor to provide error data by taking the difference between the
3 successive image representations in an image sequence.

1 11. The article of claim 10, the storage medium comprising instructions to
2 enable the processor to take the difference of two successive discrete wavelet transform
3 coded frames.

1 ~~12. The article of claim 7, the storage medium comprising instructions to~~
2 ~~enable the processor to code the bits based on whether or not the data exceeds a~~
3 ~~predetermined threshold.~~

1 13. A system comprising:
2 a device to generate error frames by differencing two successive frames
3 and to represent the error frames as a collection of ordered bits; and
4 an element to code the bits of each order to indicate zerotree roots that are
5 associated with the order.

1 14. The system of claim 13 wherein said device includes a processor and a
2 storage medium storing instructions to enable the processor to provide error data that
3 indicate motion in the image, represent the error data as a collection of ordered bits, and
4 encode the bits of each order to indicate zerotree roots that are associated with the order.

1 15. The system of claim 13 wherein said device codes said bits based on
2 whether or not the data exceeds a predetermined threshold value.